

MAR 29 2005



FAX TRANSMITTAL SHEET

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Number of Pages (including this page)

Date: March 29, 2005

To: Khai M. Nguyen - 2819

Location: United States Patent and Trademark Office

Fax No.: 703-872-9306

From: Robert L. King - 30,185

Subject: 10/804,453 - Ahmad H. Atriss et al.

Attny Docket No. SC13166TC

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MESSAGE:

Enclosed herewith, please find FORMAL AMENDMENT for filing in the below-identified application. If Applicant has overlooked any fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit 503079, Freescale Semiconductor, Inc.

ALL ITEMS MARKED WITH AN "X" ARE INCLUDED:

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PLEASE GIVE THESE PAPERS TO:

EXAMINER: Khai M. Nguyen
GROUP ART UNIT: 2819
SERIAL NO.: 10/804,453
FILED: MARCH 19, 2004
INVENTOR: AHMAD H. ATRISS ET AL.

MAR 29 2005
DOCKET NO. SCI3166TC

UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S)	Ahmad H. Atriss et al.	GROUP ART UNIT: 2819
APPLN. NO.:	10/804,453	EXAMINER: Khai M. Nguyen
FILED:	March 19, 2004	CONFIRMATION NO.: 7270
TITLE:	OPTIMIZED REFERENCE VOLTAGE GENERATION USING SWITCHED CAPACITOR SCALING FOR DATA CONVERTERS	

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AMENDMENT UNDER 37 C.F.R. 1.115

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This paper is responsive to the Office Action dated February 22, 2005 having a shortened statutory period of three months expiring May 22, 2005. Further examination and reconsideration are respectfully requested in view of the election made below.

FEES

No additional fees are believed to be owed in connection with this response. However, Applicants continue to authorize the payment of any fees or the credit of any refunds in connection with this application from or to Deposit Account No. 503079 (Freescale Semiconductor Inc.).